



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
-----------------	-------------	----------------------	---------------------	------------------

10/802,914

03/18/2004

Kazutoshi Funahashi

2004_0425A

5409

513 7590 08/13/2008

WENDEROTH, LIND & PONACK, L.L.P.

2033 K STREET N. W.

SUITE 800

WASHINGTON, DC 20006-1021

EXAMINER

RUTZ, JARED IAN

ART UNIT

PAPER NUMBER

2187

MAIL DATE

DELIVERY MODE

08/13/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

<p align="center">Advisory Action Before the Filing of an Appeal Brief</p>	Application No. 10/802,914	Applicant(s) FUNAHASHI ET AL.	
	Examiner JARED I. RUTZ	Art Unit 2187	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 31 July 2008 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☒ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
 b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. ☐ The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
 (a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);
 (b) ☐ They raise the issue of new matter (see NOTE below);
 (c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 (d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
 5. ☐ Applicant's reply has overcome the following rejection(s): _____.
 6. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
 7. ☒ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
 The status of the claim(s) is (or will be) as follows:
 Claim(s) allowed: _____.
 Claim(s) objected to: _____.
 Claim(s) rejected: 9-11, 16-19 and 24-27.
 Claim(s) withdrawn from consideration: _____.

AFFIDAVIT OR OTHER EVIDENCE

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
 9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
 10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.
 12. ☐ Note the attached Information *Disclosure Statement*(s). (PTO/SB/08) Paper No(s). _____.
 13. ☐ Other: _____.

/Kevin L Ellis/
Acting SPE of Art Unit 2187

Continuation Sheet

Applicant's arguments submitted 7/31/2008 have been carefully and fully considered, but are not found persuasive.

With respect to Applicant's arguments that Lasserre does not teach the storing of structure data, but rather the writing of a 16-bit value, see the fourth and fifth paragraphs beginning on page 7, the Examiner respectfully disagrees. The Examiner respectfully submits that there does not appear to be a limiting definition of the phrase "structure data", and accordingly it is given its broadest reasonable interpretation. Throughout the specification, words, half words, and bytes are used as examples of structure data. Accordingly, the Examiner respectfully submits that accesses to 32 bit, 16 bit and 8 bit sized data are accesses to "structure data".

With respect to Applicant's arguments that sharing data in 8 bit and 16 bit sizes is not possible in Lasserre, see the fifth paragraph on page 7, the Examiner respectfully disagrees. Applicant points to column 9 lines 22-25 showing that 16 bit and 8 bit values access different segments of memory when accessed by processors of different endianness. The Examiner respectfully points to column 9 lines 25-33 of Lasserre, which states that data objects smaller than the data portion of the external memory interface require positioning on the data bus that is offset from the most significant end of the bus if the mode is big endian and from the least significant end of the bus if the mode is little endian. It continues by teaching that these offsets are implemented by conditionally complementing byte enables based on the endianness mode included in an MMU page entry. The Examiner respectfully notes that if processors of different endianness were not accessing the same data objects smaller than 32 bits, there would be no need to generate the offsets needed to allow accesses to those objects by processors of different endianness.

With respect to Applicant's provided figures, the Examiner respectfully notes that the examples provided do not take into consideration the conditional byte complementation performed by address adjustment circuitry 706 when the endianness of the accessing processor does not correspond to the address region endianness attribute signal 710, see column 10 line 42 through column 8 line 9 of Lassarre. The Examiner respectfully notes that the details of which bits are selectively inverted are taught by Sartorius.